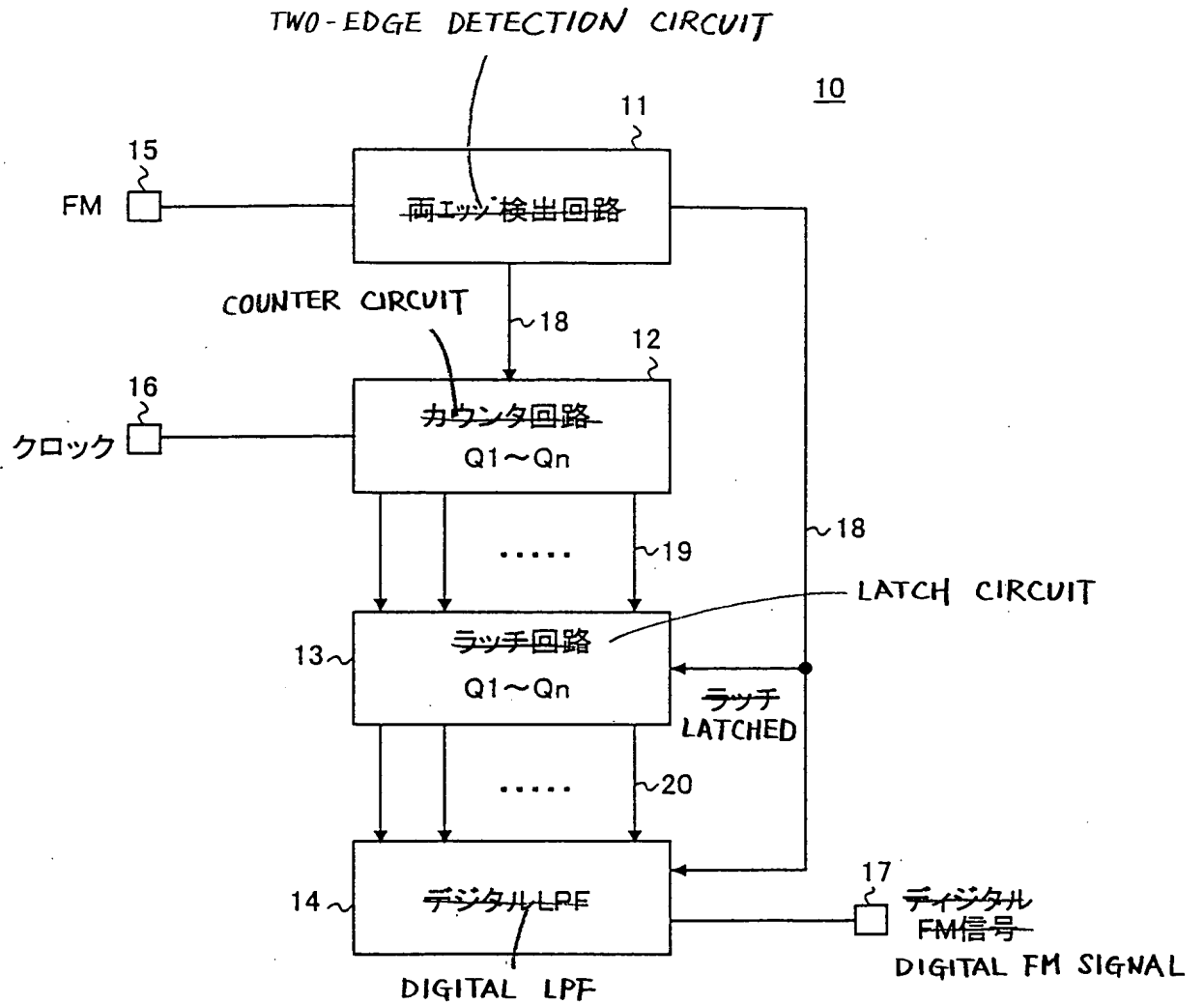


FIG. 1 PRIOR ART



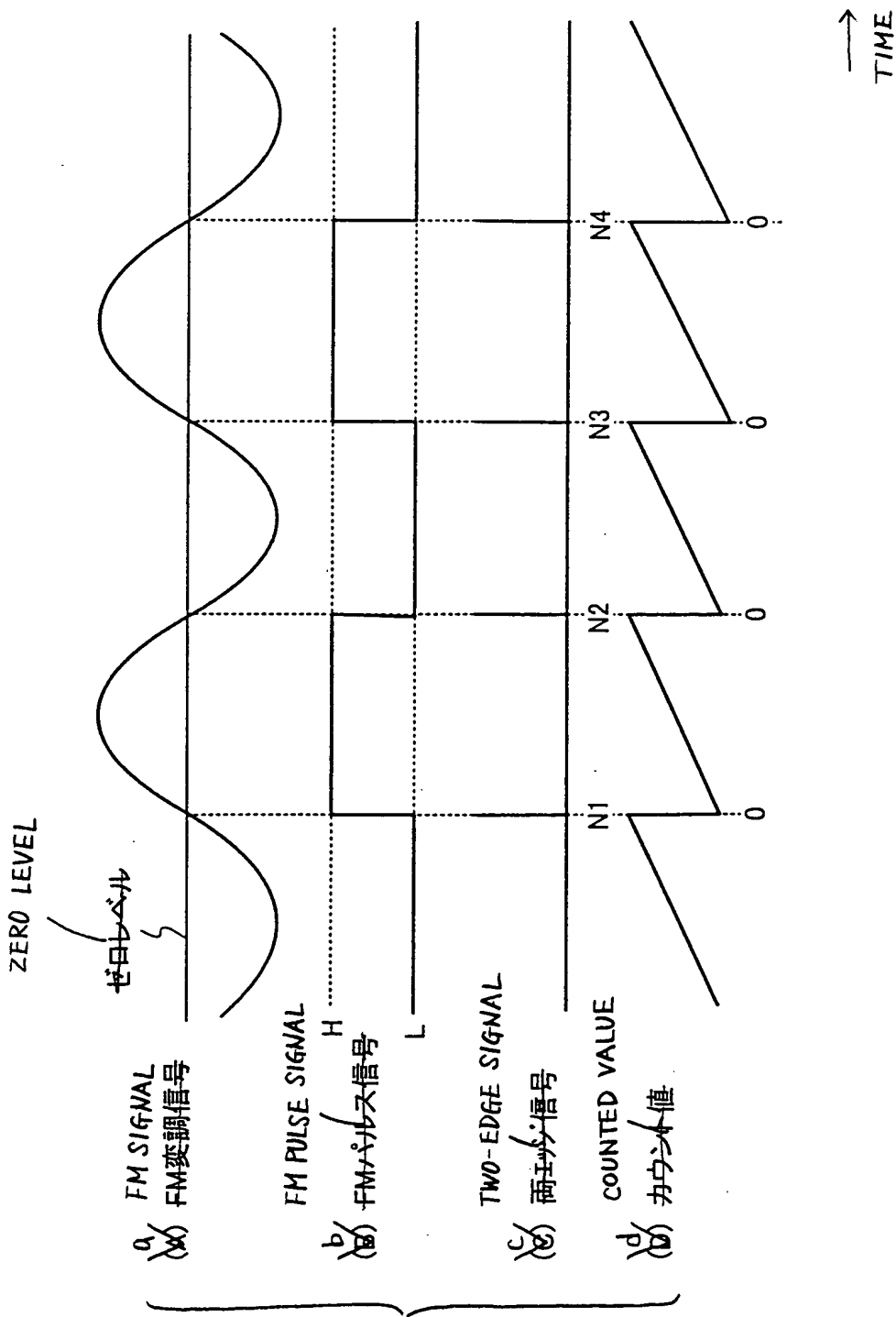
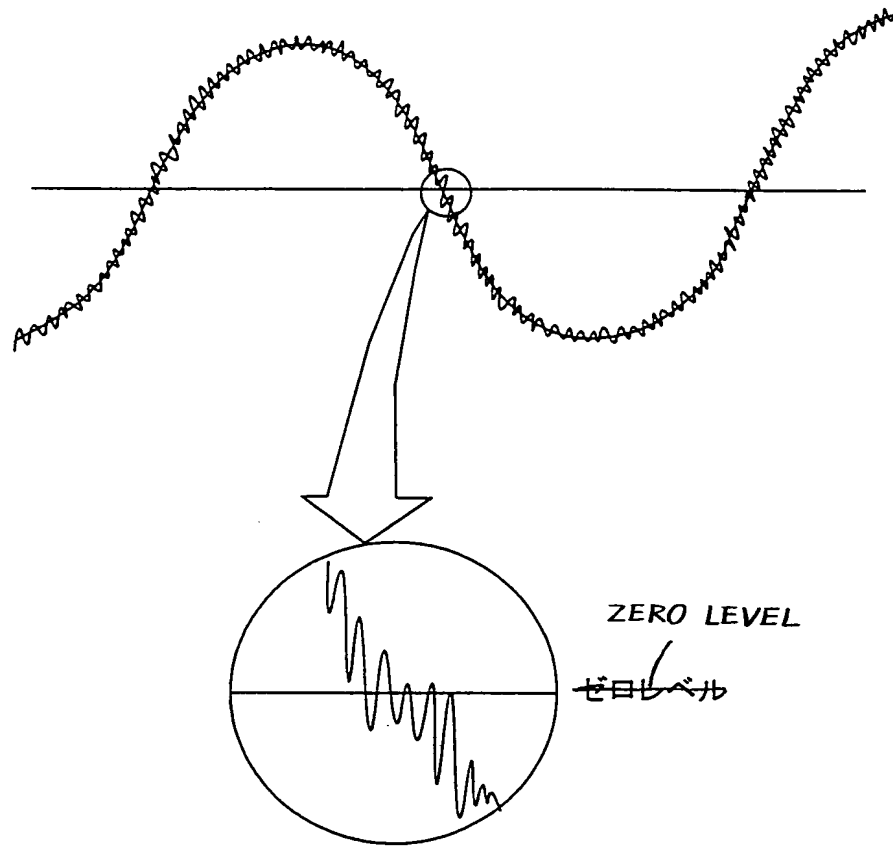


FIG. 2
PRIOR ART

FIG. 3 PRIOR ART



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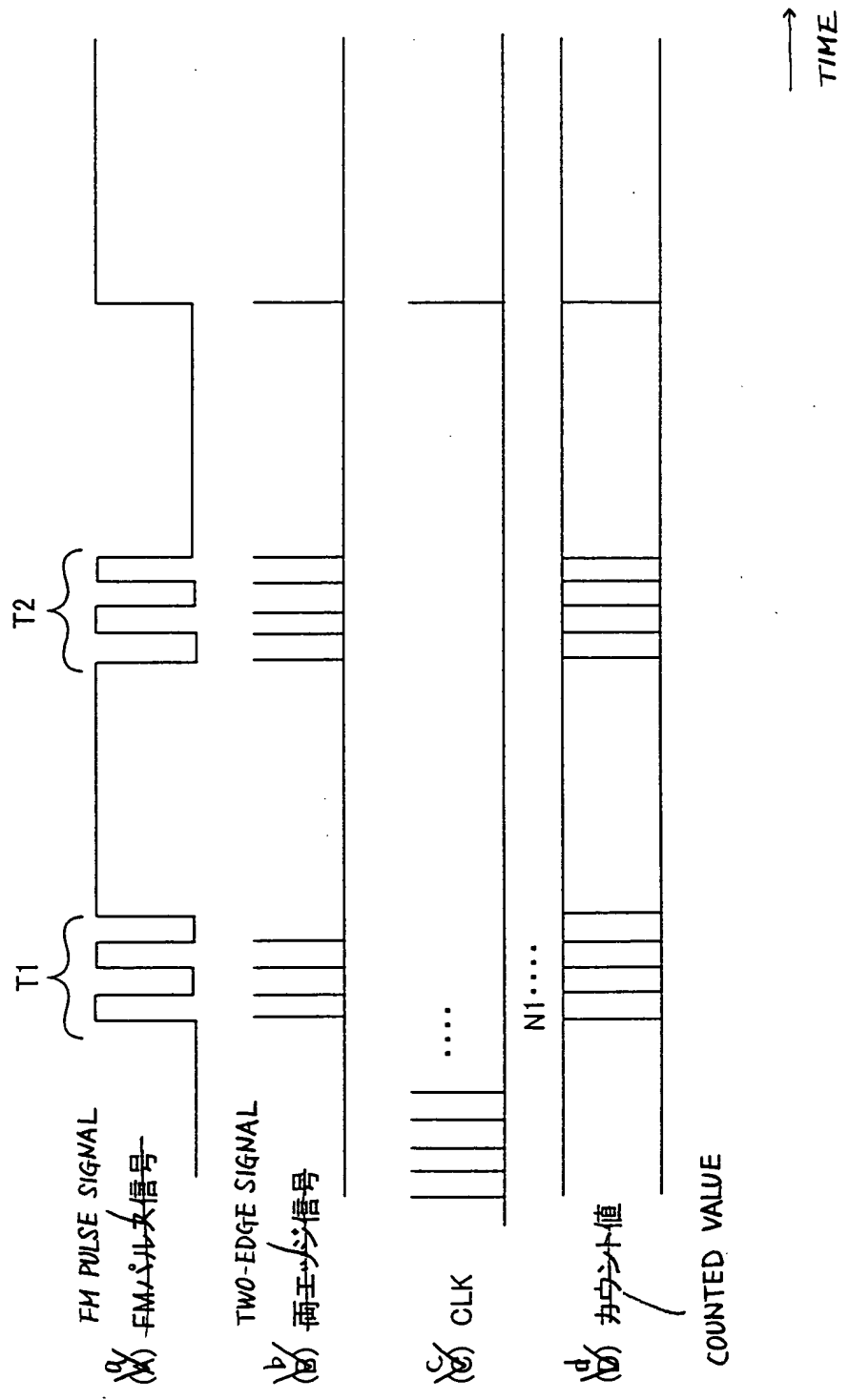
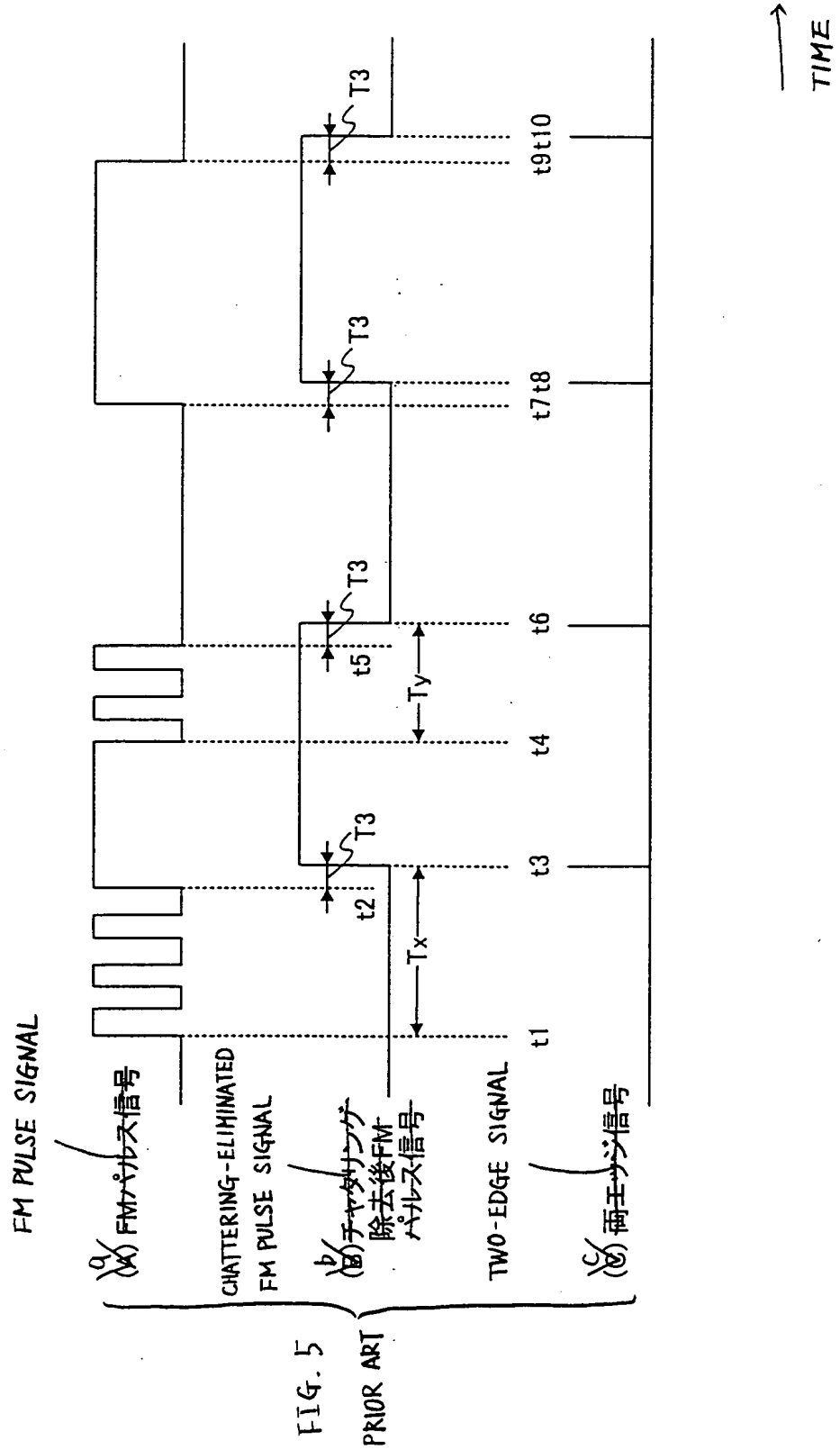


FIG. 4

PRIOR ART



MEMORY COMPENSATION CIRCUIT

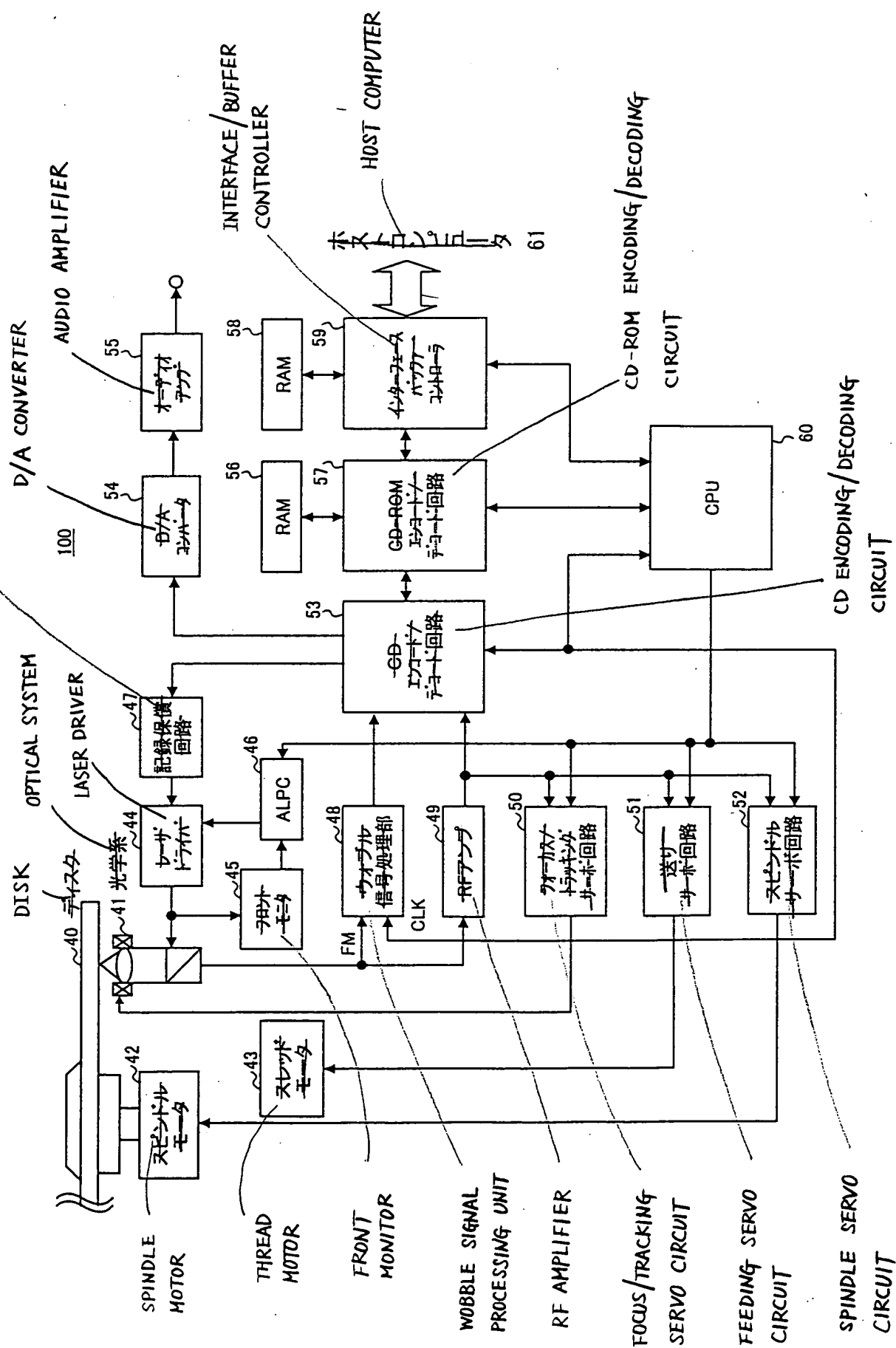
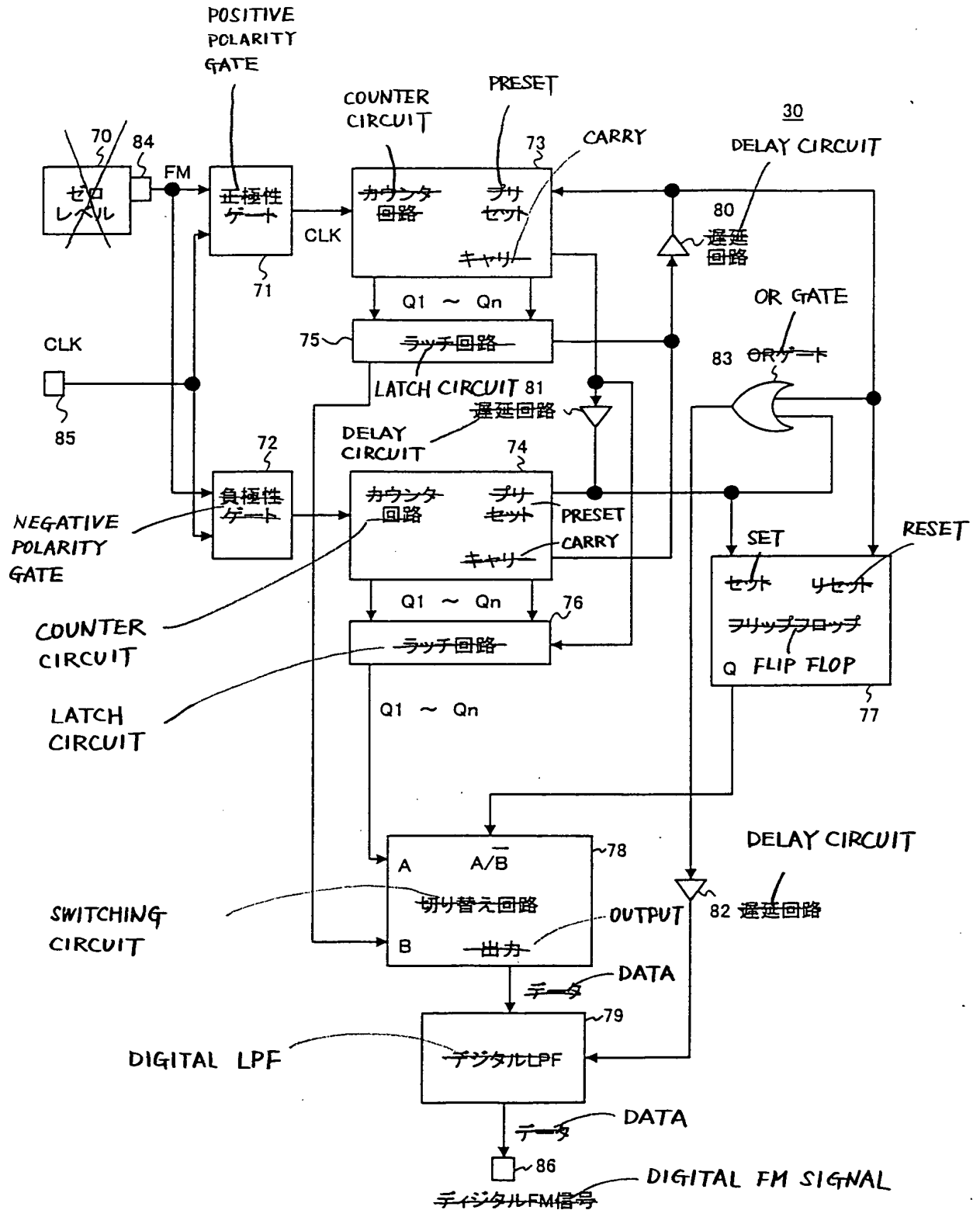


FIG. 7



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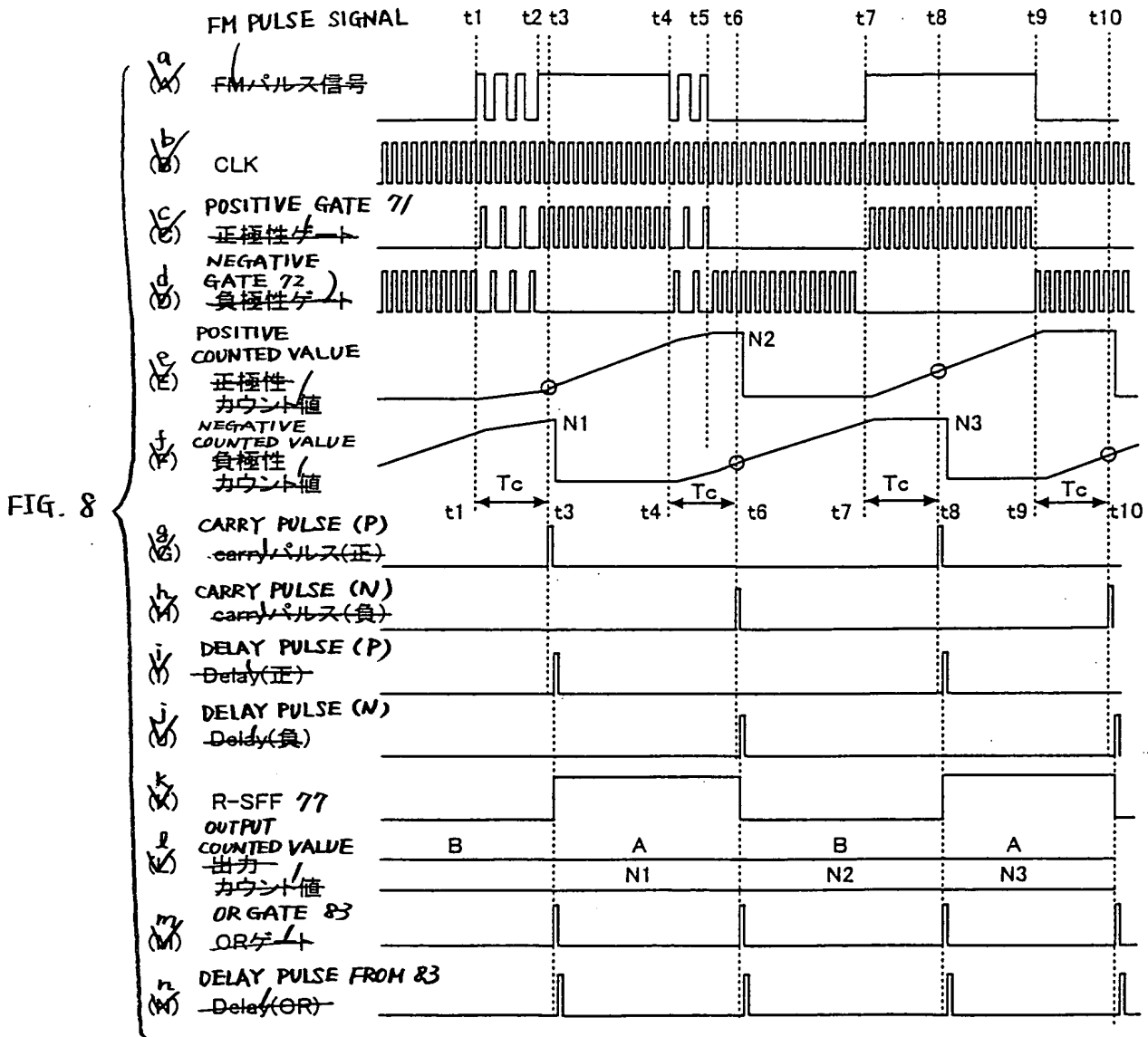
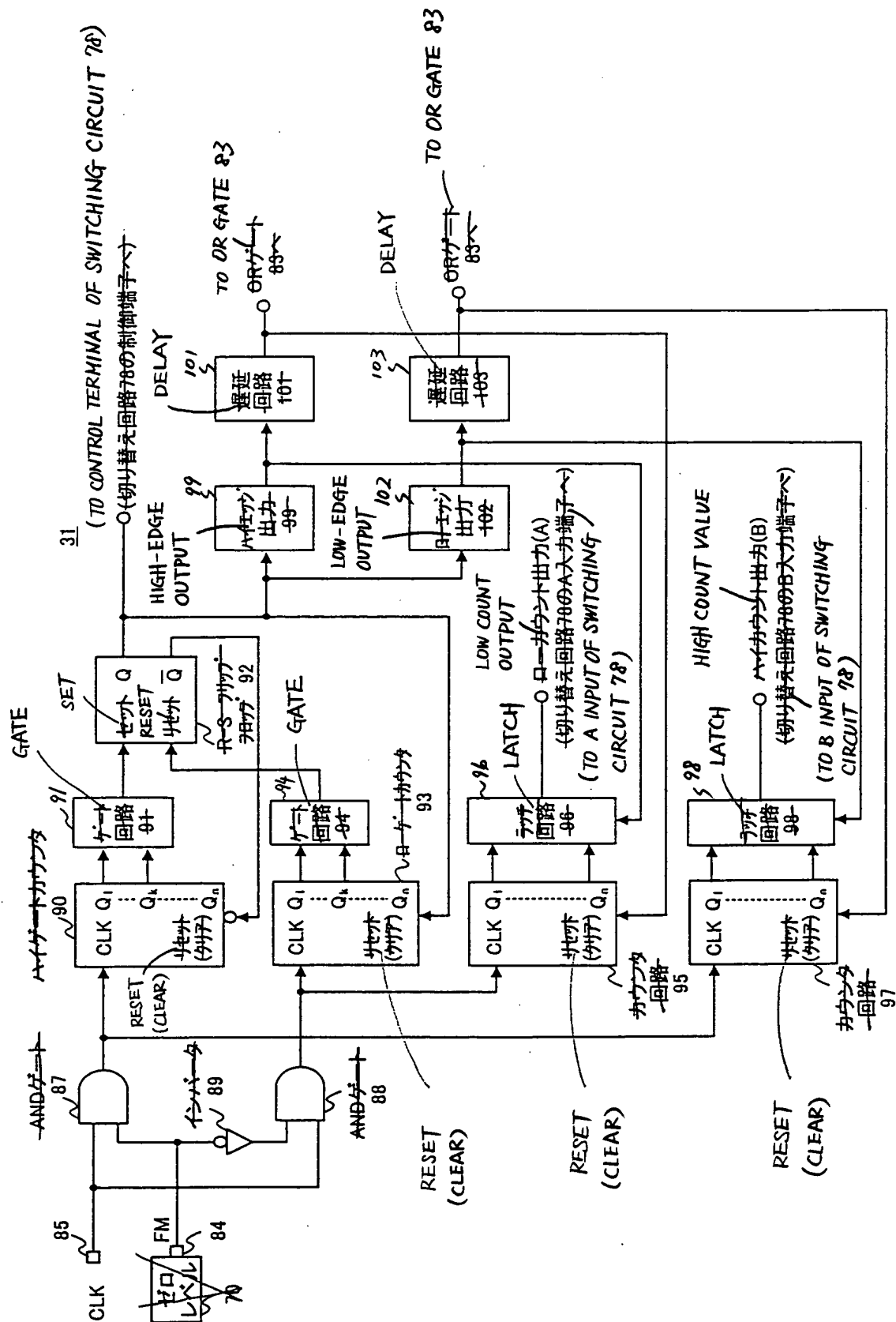
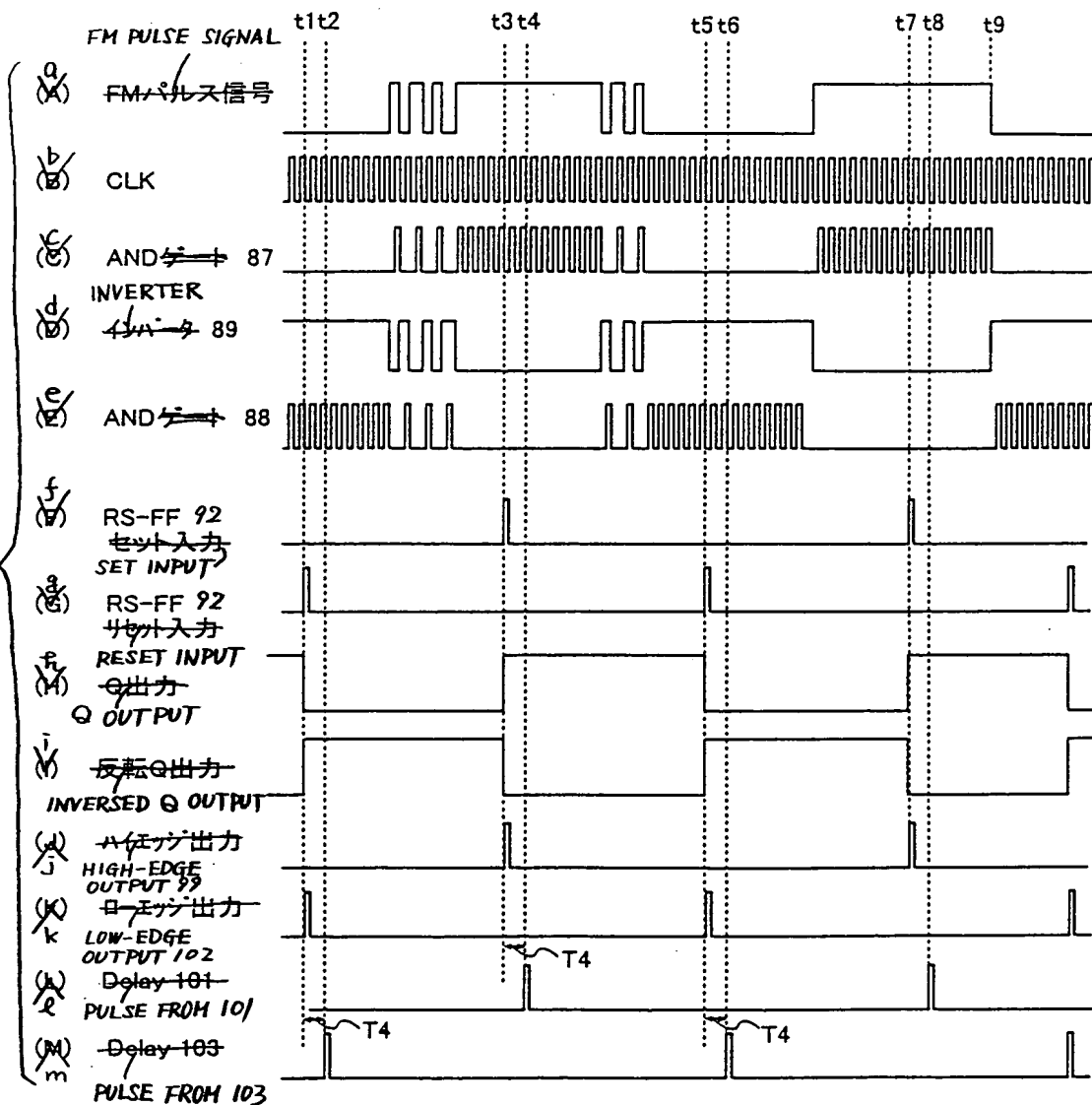


FIG. 9



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FIG. 10



90° PHASE COMPARATOR

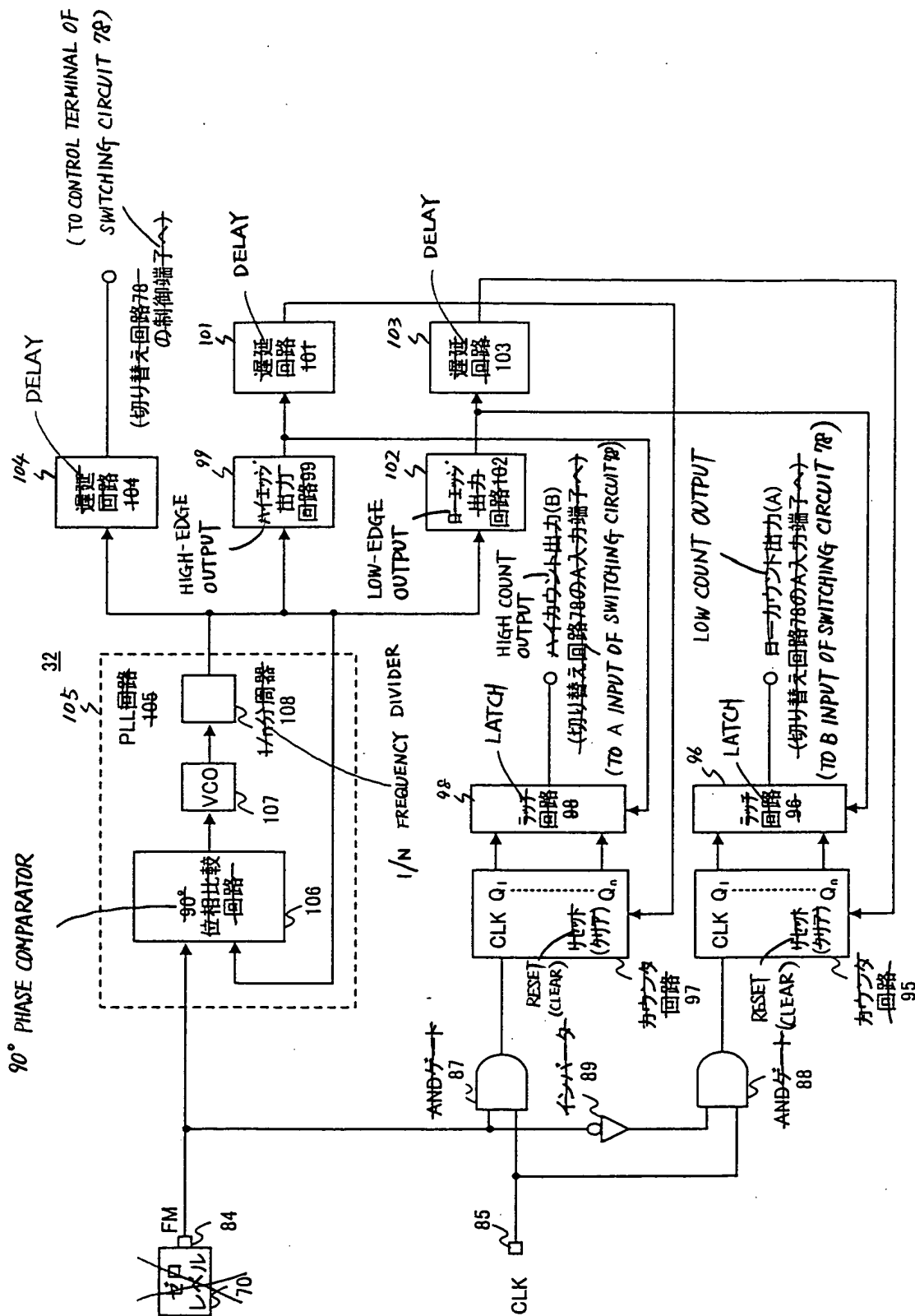


FIG. 12

